



Review of Control Strategies for Active Power Filters

Sneha Sanjay Nirmale¹, Sangeeta Mahaddalkar²

PG Student, Department of Electrical and Electronics, Goa College of Engineering, Ponda, India¹

Professor, Department of Electrical and Electronics, Goa College of Engineering, Ponda, India²

Abstract: In present days electricity has become non-separable from human life. Every sphere of human life is dependent on some or other equipment/machine in households, hospitals, industry etc. The consumers have different load patterns such as non-linear loads, switching etc. Such utility pattern results in harmonic generation and reactive power disturbances in the distribution system. The harmonics and reactive power cause a number of undesirable effects such as malfunction in sensitive equipment, over voltage by resonance, increased heat in conductor, harmonic voltage drop across the network impedance and thereby affects other customer loads connected at Point of Common Coupling (PCC). These undesirable effects are overcome to a great extent by deploying Active Power Filters (APF) at PCC. The power quality of a Distribution System can be improved by eliminating harmonics and disturbances due to reactive power. This is achieved by injecting required amount of compensating current to cancel out the harmonics and power disturbances and restore sinusoidal nature of voltage and current waveforms. This improves the power quality and helps protect the sensitive loads from malfunction thereby preventing damage. This paper presents a comprehensive review of Active Power filter configurations, their control strategies and the performance evaluation metrics such as Total Harmonic Distortion (THD) for different control strategies.

Keywords: Point of Common Coupling (PCC), Active Power Filters (APF), Total Harmonic Distortion (THD).

I. INTRODUCTION

In recent years focus have been given on the “electrical power quality” i.e. degradation of voltage and current due to harmonics, low power factor etc. The distribution system is a vital connection between generation and utilization of electrical power at rated amplitude and frequency. Non-linear loads result in drawing distorted current from the electrical power systems due to degradation of voltage and current caused by harmonics, low power factor etc. This may cause undesirable effects such as tripping of circuit breakers due to overload, overheating of distribution transformers, neutral heating due to triple n harmonics, etc. Lack of standard quality power can cause damage of equipment or even be harmful to human health resulting in loss of production. Therefore in the recent years, focus has been given on improving “Electrical Power Quality” (EPQ) [1].

Harmonics are defined as sinusoidal waveforms having frequencies that are integral multiples of the power line frequency. It is an indication of the deviation of the actual voltage or current waveform from an ideal sinusoidal waveform. Usage of power electronics converter and inverter systems has given rise to an increase in the harmonics. However the solution to this problem is also provided by power electronics through usage of Active power filters (APF), Unified power quality conditioner (UPQC), etc.

Active power filters acting as an ideal current source and injecting proper harmonic currents into the AC side can solve the problem of the load current distortion, resulting in a “clean” sinusoidal supply current. The modern power electronics provide suitable topology to mitigate the power quality problems. The typical configuration of a shunt active power filter is shown in Figure 1. A shunt active power filter minimises the harmonic content in the ac system, corrects the power factor at fundamental frequency, regulates the voltage magnitude and balance loads.

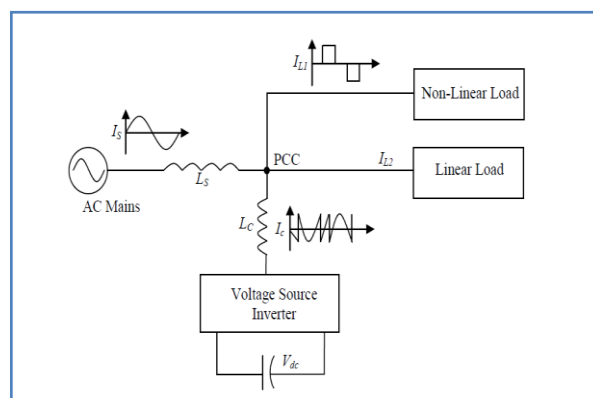


Figure 1: Basic Configuration of Shunt Active Power Filter



The paper is structured as follows. The reviews of harmonic detection method used in the paper are addressed in section II. The reviews of gating signal generation method in section III. Performance indexes for the comparison are described in section IV. Finally, section V is conclusion.

II. REFERENCE SIGNAL ESTIMATION TECHNIQUES

Many control methods are proposed in the literature to extract the reference currents from the distorted line currents. Classification according to reference current extraction techniques can be computed as time domain and frequency domain method. Frequency domain approaches are suitable for both single and three-phase systems. Time domain approaches are mainly used for three-phase systems. The first technique uses Fast Fourier Transform (FFT) to extract harmonics in frequency domain and the other technique mentioned in Fig 2. is to extract current harmonics in time domain. Fast Fourier Transform (FFT) method requires heavy calculations and requires a lot of memory where as other methods are based on the calculation of direct and indirect current components.

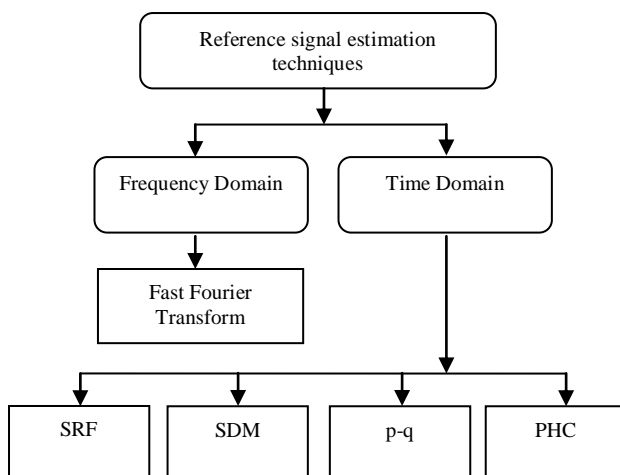


Figure 2: Shows Reference signal estimation techniques

The merit of time domain method is its fast response compared to frequency-domain hence this paper discuss time domain methods.

A. Instantaneous Reactive Power or P-Q Theory

In 1983, Akagi et al. [4-5] have proposed the Generalized Theory of the Instantaneous Reactive Power in Three-Phase Circuits, also known as p-q theory. It is based in instantaneous values in three-phase power systems, and is valid for steady-state or transitory operations, as well as for generic voltage and current waveforms, allowing the control of the active filters in real-time. Another advantage of this theory is the simplicity of its calculations, since

only algebraic operations are required. The only exception is in the separation of some power components in their mean and alternating values. The p-q theory consists of an algebraic transformation (Clarke transformation) of the three-phase voltages and currents in the a-b-c coordinates to the α - β coordinates, followed by the calculation of the p-q theory instantaneous active and reactive powers components [6-7]. This scheme is most widely used because of its fast dynamic response but gives inaccurate results under distorted and asymmetrical source conditions. p-q theory compensates the harmonic currents, corrects the power factor, compensates dynamically, and instantaneously, the zero-sequence current [2]. Also balances and reduces the values of the currents supplied by the source to the load. Since p-q method is frequency variant, on providing additional PLL circuit it becomes frequency independent.

B. Synchronous Reference Frame (SRF) Theory

The synchronous reference frame theory also known as d-q theory is based on time-domain reference signal estimation techniques. In this theory, the compensation signals are calculated based on a synchronously rotating reference frame. The synchronous reference frame theory or d-q theory performs the operation in steady-state or transient state as well as for generic voltage and current waveforms. The active power filters controlling is done in real-time system using d-q theory. SRF theory is used to extract the three-phase reference currents (i_{ca}^* , i_{cb}^* , i_{cc}^*) used by the active power filters [2]. Another important characteristic of this theory is the simplicity of the calculations, which involves only algebraic calculation. Large numbers of synchronization problems with unbalanced and non sinusoidal voltages are also avoided because it is observed that instantaneous active and reactive current id-iq method always give better result under un-balanced and non-sinusoidal voltage conditions over the instantaneous active and reactive power p-q method. It is found from study of various present techniques for reduction of current harmonic, SRF Theory is the best for both near load and non linear load and also for non regular source [3].

C. Synchronous Detection Method (SDM)

In SDM [8], it is assumed the source currents (i_{sa} , i_{sb} , i_{sc}) are balanced after compensation. Then the three-phase reactive powers are calculated from the source voltages. Then the three-phase source currents are calculated. Finally the reference currents are calculated for the active power filters Using SDM. But the performance p-q theory is much faster than compensation using SDM. It is proved that p-q theory compensates the undesirable current components within 1st cycle whereas SDM takes about 14 cycles (approximately 0.23 seconds for 50 Hz source) [9]. So for faster power quality improvement SDM is not preferable.



D. Perfect Harmonic Cancellation (PHC)

PHC strategy is capable of simultaneous mitigation of harmonics distortion, compensation of fundamental reactive power, and elimination of imbalance in source current. Therefore, the source current will be in phase with the fundamental positive sequence component of the voltage at PCC.[16-17]

III.GATING SIGNAL GENERATION METHOD

In active filter applications, the inverter delivers a current which compensates for harmonics and distortions absorbed by the load. The mostly used techniques for the generation of gating pulses are Pulse Width Modulation (PWM) and Hysteresis Current Control (HCC). The switching signals thus generated are fed to VSI with a DC link capacitor across it. Based on these switching signals the inverter generates compensating current in phase opposition to the line current.

Output Voltage of the Inverter can be modified or controlled by controlling or modifying switching current, or in case of Power Switches, by controlling or modifying Gate current. The compensating current is injected back into the power line at the PCC and thus suppressing the current harmonics present in the line [1]

A. PWM Techniques

These techniques are most efficient and they control the drives of the switching devices. We get a controlled or modified AC output voltage by varying the ON and OFF time of power switches of the inverter. This method is popularly called as Pulse Width Modulation (PWM) method. A fixed DC voltage is given to the inverter and power switches are controlled by this PWM control signal to modulate input DC voltage to required AC voltage. The power switch is usually of MOSFET or IGBT. Since frequency of operation is inversely dependent upon Inverter size so to reduce the Inverter size we have to increase the switching frequency.

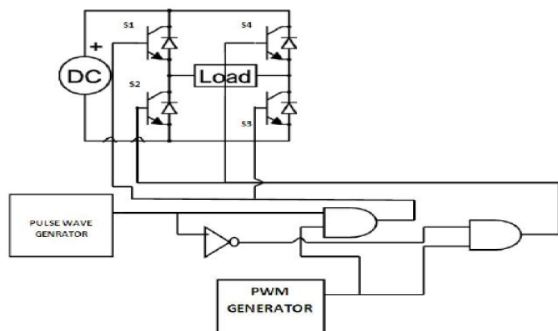


Figure 3: PWM Control of Inverter

The inverted rectangular waveform is logically ANDED with PWM waveform and is fed to power switches S2 and S4. Thus ON and OFF time of power switches are

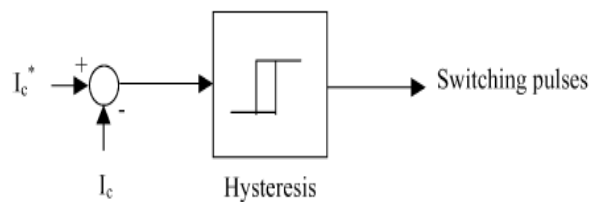
controlled by this PWM control signal. The power switch is usually of MOSFET or IGBT. The size of Inverter depends on size of these power switches.

B. Hysteresis Current Controller

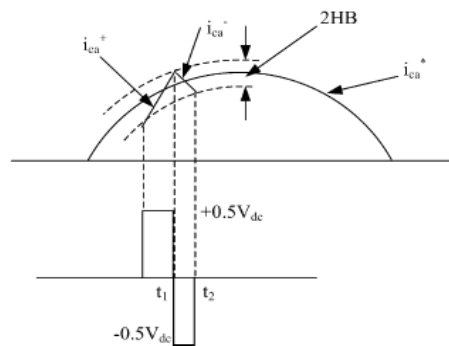
The hysteresis band current control for active power filter is used to generate the switching pattern of the inverter. There are various current control methods proposed for active power filter configurations; but the hysteresis current control method is proven to be the best among other current control methods, because of quick current controllability, easy implementation unconditioned stability, fastest control with minimum hardware.

Fig 4 consists of a hysteresis band surrounding the generated, error current. The current error is obtained by subtracting the actual filter current from the reference current. The error signal is then fed to the relay with the desired hysteresis band to obtain the switching pulses for the inverter.

A band is set above and below the generated error signal. Whenever this signal crosses the upper band, the output voltage changes so as to decrease the input current and whenever the signal crosses the lower band, the output voltage changes to increase the input current. Accordingly switching signals are generated [5]. Hysteresis band current control has two modified techniques such as adaptive hysteresis current control and Dead-beat hysteresis current control for current-fed PWM converter. [18-20]



(a)



(b)

Figure 4:(a) Conventional hysteresis band current controller

(b) Current and voltage waves with hysteresis band current control.[20]



The method minimizes mutual interference among the controls of the various phases, while requiring only an approximate knowledge of passive load parameters. Moreover, PLL synchronization ensures constant frequency operation of the inverter switches.[21]

C. Sliding Mode Controller

The sliding mode control has characteristics such as fastness, robustness and stability for large load variations. The references for the sliding mode control system are obtained by using the instantaneous reactive power theory [11]. In sliding-mode controllers, either dc-bus voltage (in a VSI) or dc-bus current (in a CSI) is maintained to the desired value and reference values for the magnitudes of the supply currents are obtained. Subtracting load currents from reference supply currents, compensating commands are derived. Sliding-mode control technique uses the nonlinear control law shown in Table I

TABLE I

Switch	is < k vs	is ≥ k vs
i	0	1
ii	1	0
Switch	vs ≤ 0	vs > 0
iii	1	0
iv	0	1

Here, i_s source current, v_s source voltage, k scaling factor and 0 – Low Level, 1 –High Level

IV. QUANTITATIVE MEASURE

The relevant performance indexes will be characterized by the total harmonic distortion (THD) of the mains current, as mentioned in the Table II.

TABLE II Comparison of The Different Control Strategies

Compensation signal Generation method	Gating signal generation method	THD	Remark
SRF /d-q	PWM	2.27% (under balanced sinusoidal voltage)	three phase four wire system, id-iq method lead always better result under un-balanced and non-sinusoidal voltage conditions over p-q method [12]
SRF /d-q	PWM	5.18% (under Un-balanced sinusoidal voltage)	
SRF /d-q	PWM	3.01% (under balanced non sinusoidal voltage)	
p-q	PWM	2.41% (under balanced sinusoidal voltage)	
p-q	PWM	7.04% (under Un-balanced sinusoidal voltage)	
p-q	PWM	9.07% (under balanced non sinusoidal voltage)	
SRF / d-q	PWM	8.4% (unbalanced load current)	
SRF / d-q	Double HCC	1.0%	
SRF / d-q	HCC	2.82%	
SRF / d-q	Dead-beat control	0.041% (Ideal Mains Voltage)	Switching losses are reduced with dead beat control and results demonstrate the superior capability of PHC strategy in reducing source current distortion [6][16][17]
SRF / d-q	Dead-beat control	4.23% (Balanced and Distorted Source Voltages)	
p-q	Dead-beat control	0.041% (Ideal Mains Voltage)	
p-q	Dead-beat control	25.79% (Balanced and Distorted Source Voltages)	
PHC	Dead-beat control	0.044% (Ideal Mains Voltage)	In adaptive HCC method, the instantaneous switching frequency remains constant with little deviation in order to determine switching device and its switching losses [20]
PHC	Dead-beat control	0.045% (Balanced and Distorted Source Voltages)	
PHC	HCC	4.6% (under heavy nonlinear load)	
SRF / d-q	Adaptive HCC	4.48%	



SRF / d-q	Sliding Mode	Less than 2%	Good regulation in DC bus for load variation [11]
SD		0.41%	DQ method provides the minimum %THD after compensation compared with SD[8]
SRF / d-q		0.003%	

V. CONCLUSION

A brief review about the control strategies of (p - q; d -q, SD and PHC) for active power filters have been made taking the THD as the quantitative measure. Among the four controllers, p-q, SD and d-q strategies are the most sensitive to voltage distortion at PCC. The results show that the PHF method provides the minimum %THD after compensation compared with other methods.

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